
Pikuniku Activation Code [key Serial]

the mindy kaling show - season 1 - all episodes.rar winrar is a multi-bit stream rar archiver. it can contain files from different rar archives. winrar supports rar, zip, 7z, ace, cab, arj, tar, gz, iso, bzip2, tar, ntfs, iso, and files from fat, exfat and ntfs partitions. the program can unpack rar archives without reading the entire archive. 1. field of the invention the present invention relates to a semiconductor device including a nand type flash memory. 2. description of the related art a flash memory is one of nonvolatile memories. the flash memory is characterized by the use of tunneling phenomenon in a silicon substrate to make a threshold voltage of a memory cell variable. an nand type flash memory is known as a typical flash memory. the nand type flash memory is characterized by allowing a large number of memory cells to be connected in series to achieve high integration. a memory cell in the nand type flash memory includes a control gate and a floating gate. a high voltage is applied to the control gate to inject hot electrons into the floating gate. as a result, data is written to a memory cell. at the time of the data write operation, a step-up voltage higher than a supply voltage is applied to a word line connected to the control gate of a selected memory cell to make a potential of the word line different from those of word lines connected to nonselected memory cells. as a result, a gate potential of the memory cell is made different from those of the other memory cells. the threshold voltage of the memory cell changes as the gate potential changes. as a result, data is written to the memory cell. at the time of the data read operation, a read voltage is applied to a selected word line and the data write voltage is applied to a nonselected word line. in this case, as the read voltage is made higher, data stored in a memory cell can be read out with increased accuracy. increasing the supply voltage in the memory cell is effective to reduce power consumption of the flash memory. however, as the supply voltage is increased, the step-up voltage applied to the word line is also increased. an excessive voltage applied to the word line may cause the breakdown of the gate oxide film of the memory cell. as a result, data write efficiency is reduced. japanese patent application laid-open publication no. 2004-021569 discusses a technique for suppressing the supply voltage of a memory cell from increasing to suppress an excessive voltage. specifically, japanese patent application laid-open publication no. 2004-021569 discusses a method for improving the writing efficiency by slightly increasing a supply voltage of a word line adjacent to a selected word line to prevent the occurrence of the excessive voltage of the word line. meanwhile, in recent years, there is a demand for a flash memory that can operate at a lower voltage as the power supply voltage is reduced. for example, there is a demand for the flash memory that can operate at a supply voltage of 2 v or lower. however, in the technique discussed in the above-mentioned japanese patent application laid-open publication no. 2004-021569, data write efficiency is reduced by increasing the supply voltage of the word line adjacent to a selected word line. as a result, the data write efficiency is reduced even when the power supply voltage is reduced.

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